

## INVENTOR INFORMATION

Inventor One Given Name:: Yoshiro  
Family Name:: IWASA  
Postal Address Line One:: c/o Seiko Epson Corporation  
Postal Address Line Two:: 3-5, Owa 3-chome  
City:: Suwa  
State or Province:: Nagano  
Country:: Japan  
Postal or Zip Code:: 392-8502  
Country of Residence:: Japan  
Citizenship Country:: Japan

## CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 27572  
Fax One:: (248) 641-0270  
Electronic Mail One:: bewade@hdp.com

## APPLICATION INFORMATION

Title Line One:: LEAD FRAME, SEMICONDUCTOR CHIP PACKAGE,  
Title Line Two:: METHOD FOR MANUFACTURING SEMICONDUCTOR D  
Title Line Three:: EVICE, AND SEMICONDUCTOR DEVICE  
Total Drawing Sheets:: 18  
Formal Drawings?:: Yes  
Application Type:: Utility  
Docket Number:: 9319S-000666  
Secrecy Order in Parent Appl.?:: No

## REPRESENTATIVE INFORMATION

Representative Customer Number:: 27572

## PRIOR FOREIGN APPLICATIONS

Foreign Application One:: 2003-044360  
Filing Date:: 02-21-2003  
Country:: Japan  
Priority Claimed:: Yes

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